

Split Core Hall Effect AC/DC Current Sensor CYHCS-EKC

This Hall Effect current sensor is based on open loop principle and designed with a split core and a high galvanic isolation between primary conductor and secondary circuit. It can be used for measurement of AC/DC current etc. The output of the transducer reflects the real wave of the current carrying conductor. It can be mounted on the primary cable directly.

Product Characteristics	Applications
 Excellent accuracy Very good linearity Light in weight Less power consumption Window structure, easily mounting Electrically isolating the output of the transducer from the current carrying conductor No insertion loss Current overload capability 	 Photovoltaic equipment Frequency conversion timing equipment Various power supply Uninterruptible power supplies (UPS) Electric welding machines Numerical controlled machine tools Electrolyzing and electroplating equipments Electric powered locomotive Microcomputer monitoring Electric power network monitoring

Electrical Data

Primary Nominal	Primary Current	Output Voltage	Part number
Current I_r (A)	Measuring Range $I_p(A)$	(Analog) (V)	
30A	0 ~ ± 60A	4 ±1.0%	CYHCS-EKC-30A
50A	0 ~ ± 100A		CYHCS-EKC-50A
80A	0 ~ ± 160A		CYHCS-EKC-80A
100A	0 ~ ± 200A		CYHCS-EKC-100A
200A	0 ~ ± 400A		CYHCS-EKC-200A
300A	0 ~ ± 600A		CYHCS-EKC-300A

 V_{cc} =±12V~±15VDC ± 5% Supply Voltage: **Current Consumption** I_c < 25mA at Vcc=±15VDC Isolation Voltage 2,5kV, 50/60Hz, 1min Output Voltage at I_r , T_A =25°C: $V_{\text{out}} = 4 \text{VDC}$ Output Impedance: $R_{\rm out}$ < 150 Ω Load Resistor: $R_{\rm l} > 10 \text{k}\Omega$ Accuracy at I_r , $T_A=25^{\circ}$ C (without offset), X < 1.0% Linearity from 0 to I_r , $T_A=25$ °C, E_{l} <1.0% FS Electric Offset Voltage, T_A =25°C, $V_{oe} < \pm 25 \text{mV}$ Magnetic Offset Voltage $(I_r \rightarrow 0)$ $V_{om} < \pm 25 \text{mV}$ Thermal Drift of Offset Voltage, -25°C~+85°C $V_{ot} < \pm 1.0 \text{mV/}^{\circ}\text{C}$ Thermal Drift (-10°C to 50°C), T.C. $< \pm 0.1\%$ /°C Response Time at 90% of I_P (f=1k Hz) $t_r < 7 \mu s$ $f_b = DC-20 \text{ kHz}$ Frequency Bandwidth (-3dB),

General Data

Ambient Operating Temperature, $T_A = -25^{\circ}\text{C} \sim +85^{\circ}\text{C}$ Ambient Storage Temperature, $T_S = -40^{\circ}\text{C} \sim +100^{\circ}\text{C}$ Unit weight: 43g / unit

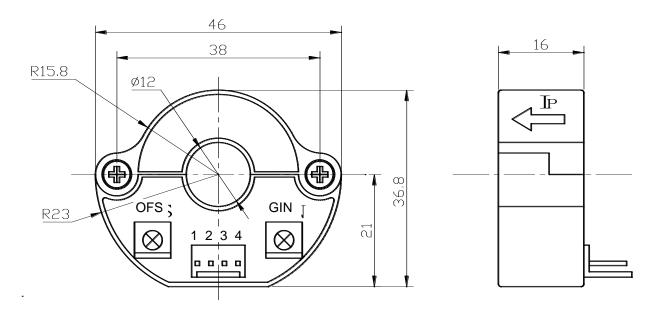
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PIN Definition and Dimensions



OFS: Offset Adjustment

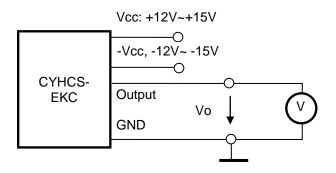
GIN: Gain Adjustment

Pin arrangement:

1 (V+): Vcc

2 (V-): -Vcc

3 (OUT): OUTPUT 4 (GND): 0V (GND)





Notes:

- 1. Connect the terminals of power source, output respectively and correctly, never make wrong connection.
- 2. Two potentiometers can be adjusted, only if necessary, by turning slowly to the required accuracy with a small screwdriver.
- 3. The best accuracy can be achieved when the window is fully filled with bus-bar (current carrying conductor).
- 4. The in-phase output can be obtained when the direction of current of current carrying conductor is the same as the direction of arrow marked on the transducer

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