

Split Core Hall Effect DC Current Sensor CYHCT-KCV

This Hall Effect current sensor is based on open loop principle and designed with a split core and a high galvanic isolation between primary conductor and secondary circuit. It can be used for measurement of DC current etc. The output of the transducer reflects the real wave of the current carrying conductor.

Product Characteristics	Applications
 Excellent accuracy Very good linearity Light in weight Less power consumption Window structure with split core Electrically isolating the output of the transducer from the current carrying conductor No insertion loss Current overload capability 	 Photovoltaic equipment Frequency conversion timing equipment Various power supply Uninterruptible power supplies (UPS) Electric welding machines Numerical controlled machine tools Electrolyzing and electroplating equipment Electric powered locomotive Microcomputer monitoring Electric power network monitoring

Electrical Data/Input

Primary Nominal DC Current <i>I_r</i> (A)	Primary Current Measuring Range I _p (A)	Output Voltage (V)	Part number
. , ,	<u> </u>		OVIJOT KOV 11/D4000A
1000A	0 ~ ± 1000A	x=0: 0-4V ±1.0% x=3: 0-5V ±1.0% CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC CYHC	CYHCT-KCV-U/B1000A-xn
2000A	0 ~ ± 2000A		CYHCT-KCV-U/B2000A-xn
3000A	0 ~ ± 3000A		CYHCT-KCV-U/B3000A-xn
4000A	0 ~ ± 4000A		CYHCT-KCV-U/B4000A-xn
5000A	0 ~ ± 5000A		CYHCT-KCV-U/B5000A-xn
6000A	0 ~ ± 6000A		CYHCT-KCV-U/B6000A-xn
8000A	0 ~ ± 8000A		CYHCT-KCV-U/B8000A-xn
10000A	0 ~ ± 10000A	3. 2 3 ,	CYHCT-KCV-U/B10000A-xn

(n=2, *Vcc*= +12VDC; n=3, *Vcc* =+15VDC; n=4, *Vcc* =+24VDC, n=5, *Vcc* =±12VDC, n=6, *Vcc* =±15VDC, n=7, *Vcc* =±24VDC, U: unidirectional, B: bidirectional)

Supply Voltage: V_{cc} =+12V, +15V, +24V± 5% Current Consumption I_c < 50mA

Isolation Voltage 6kV, 50/60Hz, 1min

Electrical Data/Output

Output Voltage at I_r , T_A =25°C: V_{out} =0- 4V, 0-5V, 0-10VDC Output Impedance: R_{out} < 150 Ω

Load Resistor: $R_L > 10 \text{k}\Omega$

Accuracy

Accuracy at I_r , $T_A=25^{\circ}$ C (without offset), X < 1.0% Linearity from 0 to I_r , $T_A=25$ °C, E_{l} <1.0% FS V_{oe} <25mV Electric Offset Voltage, T_A =25°C, Magnetic Offset Voltage $(I_r \rightarrow 0)$ $V_{om} < \pm 30 \text{mV}$ Thermal Drift of Offset Voltage, $V_{ot} < \pm 1.0 \text{mV/}^{\circ}\text{C}$ Thermal Drift (-10°C to 50°C), T.C. $< \pm 0.1\%$ /°C Response Time at 90% of I_P (f=1k Hz) $t_r < 1 \text{ms}$ Frequency Bandwidth (-3dB), $f_b = DC-3 \text{ kHz}$

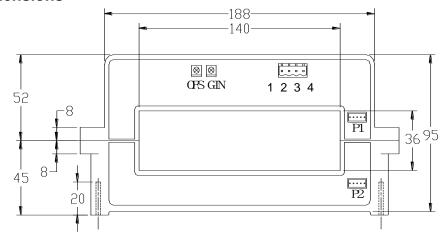
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General Data

Ambient Operating Temperature, Ambient Storage Temperature,

$$T_A$$
 = -25°C ~ +85°C
 T_S = -40°C ~ +100°C

PIN Definition and Dimensions

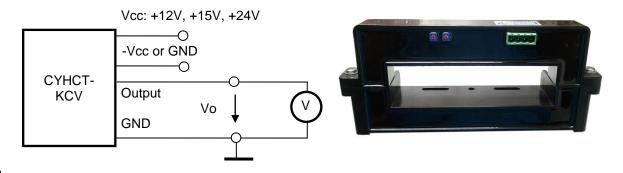


GIN: Gain Adjustment

OFS: Offset Adjustment

Pin arrangement:

1(V+): Vcc 207 2(V-): -Vcc or GND 197 182 3(OUT): **OUTPUT** 146 90 4(GND): 0V (GND) 2-M5-5.5 ***** 48 ø5.5 39.5 28 **IP** 18



Notes:

- 1. Connect the terminals of power source, output respectively and correctly, never make wrong connection.
- 2. Two potentiometers can be adjusted, only if necessary, by turning slowly to the required accuracy with a small screwdriver.
- 3. The best accuracy can be achieved when the window is fully filled with bus-bar (current carrying conductor).
- 4. The in-phase output can be obtained when the direction of current of current carrying conductor is the same as the direction of arrow marked on the transducer